

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	106569	(multiplexer)	USPAT	OR	ON	2010/03/04 13:49
L2	8879	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)))	USPAT	OR	ON	2010/03/04 13:49
L3	1236	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))	USPAT	OR	ON	2010/03/04 13:49

L4	629	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3)	USPAT	OR	ON	2010/03/04 13:49
L5	276	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2010/03/04 13:49

L6	96	(multiplexer\$1) AND (((multiple) or (plural\$5) or (several)) adj ((input\$1) or (channel\$1))) AND ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)))	USPAT	OR	ON	2010/03/04 13:49
L7	16	(multiplexer\$1) AND (((multiple) or (plural\$5) or (several)) adj ((input\$1) or (channel\$1))) AND ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj	USPAT	OR	ON	2010/03/04 13:49

		((identifier) or (identification) or (ID)))				
L8	27	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2010/03/04 13:49
L9	13	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND	USPAT	OR	ON	2010/03/04 13:49

		(((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID))) AND ((output) adj ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
L10	16	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND (((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID))) AND ((output) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))	USPAT	OR	ON	2010/03/04 13:49

L11	27	(multiplexer\$1) AND (((multiple) or (plural\$5) or (several)) adj ((input\$1) or (channel\$1))) AND ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2010/03/04 13:49
L12	122	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel\$1))) AND ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj	USPAT	OR	ON	2010/03/04 13:49

		(memory) adj (access)) or (DMA) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC))				
L13	52	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2010/03/04 13:49
L14	51	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND	USPAT	OR	ON	2010/03/04 13:49

		((least) adj (significant) adj (bit)) or (LSB)) AND ((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
L15	51	((multiplexer\$1) AND ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND ((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or	USPAT	OR	ON	2010/03/04 13:49

		(source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
L16	51	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))	USPAT	OR	ON	2010/03/04 13:49

L17	2268	((370/535) or (370/536) or (370/539) or (370/540) or (370/541) or (370/542) or (370/544)).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L18	1072	(370/535).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L19	308	(370/536).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L20	731	(370/537).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L21	158	(370/539).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L22	122	(370/540).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L23	81	(370/541).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L24	171	(370/542).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L25	44	(370/544).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L26	9297	((multiplexer\$1) AND ((input\$1) or (channel \$1)) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))	USPAT	OR	ON	2010/03/04 13:49

L27	5557	(((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1))) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)))	USPAT	OR	ON	2010/03/04 13:49
L28	244	(((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)))	USPAT	OR	ON	2010/03/04 13:49
L29	61	(((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2010/03/04 13:49

L30	6502	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) AND ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) AND (((source) adj (identification\$1)) or (identification\$1) or ("ID")) AND (priority)	USPAT	OR	ON	2010/03/04 13:49
L31	21	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or (identification\$1) or ("ID")) SAME (priority)	USPAT	OR	ON	2010/03/04 13:49
L32	635	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or (identification\$1) or ("ID") or (source\$1)) SAME ((priority) or (order\$2))	USPAT	OR	ON	2010/03/04 13:49

L33	242	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or (identification\$1) or ("ID") or (source\$1)) SAME ((priority) or (order\$2)) SAME (output\$1)	USPAT	OR	ON	2010/03/04 13:49
L34	731	(370/537).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L35	5	L32 and L34	USPAT	OR	ON	2010/03/04 13:49
L36	3	L33 and L34	USPAT	OR	ON	2010/03/04 13:49
L37	1	("7016337").PN.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L38	73500	(370/\$5).CCLS.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L39	124	L32 and L38	USPAT	OR	ON	2010/03/04 13:49
L40	65	L33 and L38	USPAT	OR	ON	2010/03/04 13:49
L41	49	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or (identification\$1) or ("ID")) SAME ((priority) or (order \$2)) SAME (output \$1)	USPAT	OR	ON	2010/03/04 13:49

L42	375	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2)) SAME (output\$1)	USPAT	OR	ON	2010/03/04 13:49
L43	498	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or	USPAT	OR	ON	2010/03/04 13:49

		(identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2))				
L44	499	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2) or ((quality) adj\$2 (services\$1)) or (QOS))	USPAT	OR	ON	2010/03/04 13:49
L45	395	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj	USPAT	OR	ON	2010/03/04 13:49

		(address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3))				
L46	396	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2010/03/04 13:49
L47	90	L46 and L38	USPAT	OR	ON	2010/03/04 13:49

L48	8	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ((((pre) adj (determin\$3)) or (predetermin\$3) or (known)) near2 (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2))) SAME ((priority) or (order \$2)) SAME ((output \$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2010/03/04 13:49
L49	10	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ((((pre) adj (determin\$3)) or (predetermin\$3) or (known)) near2 (((source) adj (identification\$1)) or	USPAT	OR	ON	2010/03/04 13:49

		((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1 or (identifier\$1 or ("ID" or (address \$2))) SAME ((priority) or (order \$2))				
L50	4	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1 or (identifier\$1 or ("ID" or (address \$2)) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent)) SAME (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2010/03/04 13:49

L51	412	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME (((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2010/03/04 13:49
L52	40	L51 AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2010/03/04 13:49
L53	1	("6898182").PN.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L54	412	(((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1))) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME (((source) adj (identification\$1)) or	USPAT	OR	ON	2010/03/04 13:49

		((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2)) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))				
L55	40	L54 AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2010/03/04 13:49
L56	1	("7310315").PN.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L57	1	("7373421").PN.	USPAT; USOCR	OR	OFF	2010/03/04 13:49
L60	763	((JL) near2 (LEE)). INV.	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/04 13:52
L62	38	L60 AND (multiplex \$3)	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/04 14:16
L64	39	L60 AND ((multiplex \$3) or (MUX))	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/04 14:21
L65	377	L60 AND (((selection) adj (circuit\$1)) or (circuit \$1) or (circuitry) or (multiplexer\$1) or (multiplexor\$1) or (MUX) or (selection \$1) or (selecting) or (pick\$3) or (choos\$3) or (chosen))	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/04 14:24

L68	1	L60 AND (((selection) adj (circuit\$1)) or (circuit \$1) or (circuitry) or (multiplexer\$1) or (multiplexor\$1) or (MUX) or (selection \$1) or (selecting) or (pick\$3) or (choos\$3) or (chosen)) AND (((high\$3) or (max) or (maximum\$1)) adj (priorit\$3)) AND (((bit\$1) near2 (sequence\$1)) or (bit \$1) or (byte\$1)) AND (((most) adj (significant) adj (bit \$1)) or ((most) adj (significant) adj (byte \$1)) or (MSB))	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/04 14:28
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EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L59	3	(((system\$1) or (method\$1) or ((direct) adj (memory) adj (access)) or (DMA) or (multiplexor\$1)) AND ((transfer\$4) or (multiplex\$3) or (transmit\$4) or (send \$3) or (sent)) AND ((signal\$4) or (stream \$3) or (packet\$1) or (cell\$1) or (frame\$1)) AND ((channel\$1) or (link\$1) or (pipe\$1) or (pipelin\$3) or (path \$1)) AND ((storage) or (queue\$1) or (buffer \$1) or (memory) or (memories) or (FIFO \$1) or (table\$1) or (register\$1)) AND (((source) adj (identification) adj (information)) or ((source) adj	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/04 13:49

		(identification)) or ((source) adj (identifier \$1)) or ((source) adj (ID\$1)) or ((source) adj (address\$2)) or (source\$1) or (port\$1) or (input\$1) or (address \$2)) AND ((order\$3) near2 (priorit\$3)) or (priorit\$3) or (prioritization\$1)) AND (((selection) adj (circuit\$1)) or (circuit \$1) or (circuitry) or (multiplexer\$3)) AND ((first) or (initial) or (primary) or (main) or (earliest) or (initial) or (priumary) or (basic) or (principal)) AND ((high\$3) adj (priorit \$3)) AND (((bit\$1) near2 (sequence\$1)) or (bit\$1) or (byte\$1)) AND (((most) adj (significant) adj (bit \$1)) or ((most) adj (significant) adj (byte \$1)) or (MSB)) .CLM.				
L67	7	(((system\$1) or (method\$1) or ((direct) adj (memory) adj (access)) or (DMA) or (multiplexor\$1) or (multiplexer\$1) or (MUX)) AND ((transfer \$4) or (multiplex\$3) or (transmit\$4)) AND ((signal\$4) or (stream \$3) or (packet\$1) or (cell\$1) or (frame\$1)) AND ((channel\$1) or (link\$1) or (pipe\$1) or (pipelin\$3) or (path \$1)) AND ((storage) or (queue\$1) or (buffer \$1) or (memory) or (memories) or (FIFO \$1)) AND (((source) adj (identification) adj (information)) or ((source) adj (identification)) or	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/04 14:27

((source) adj (identifier
 \$1)) or ((source) adj
 (ID\$1)) or (source\$1)
 or (input\$1)) AND
 (((order) near2 (priorit
 \$3)) or (priorit\$3))
 AND (((selection) adj
 (circuit\$1)) or (circuit
 \$1) or (circuitry) or
 (multiplexer\$1) or
 (multiplexor\$1) or
 (MUX) or (selection\$1)
 or (selecting) or (pick
 \$3) or (choos\$3) or
 (chosen)) AND ((first
 or (initial) or (primary)
 or (main) or (earliest)
 or (initial) or (primary)
 or (basic) or
 (principal)) AND
 (((high\$3) or (max)
 or (maximum\$1)) adj
 (priorit\$3)) AND (((bit
 \$1) near2 (sequence
 \$1)) or (bit\$1) or (byte
 \$1)) AND (((most)
 adj (significant) adj (bit
 \$1)) or ((most) adj
 (significant) adj (byte
 \$1)) or (MSB))).CLM.

3/ 4/ 2010 2:31:32 PM

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